

Figure No. 1 Memory and Storage Requirements

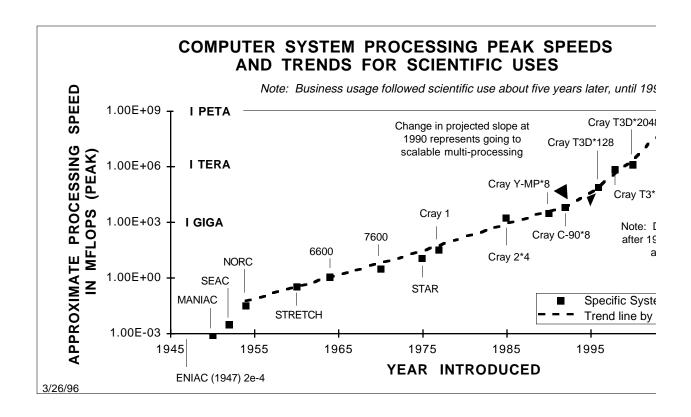


Figure No. 2 Possible Target Date for a PetaFLOPS System

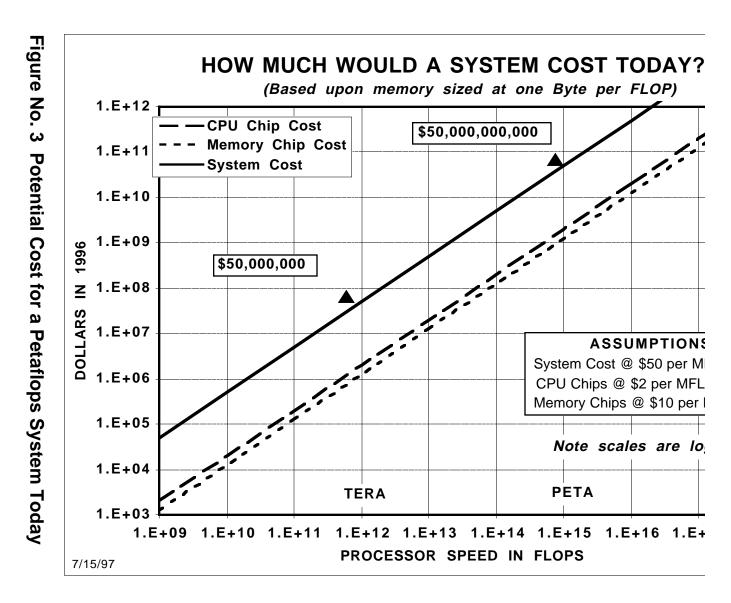
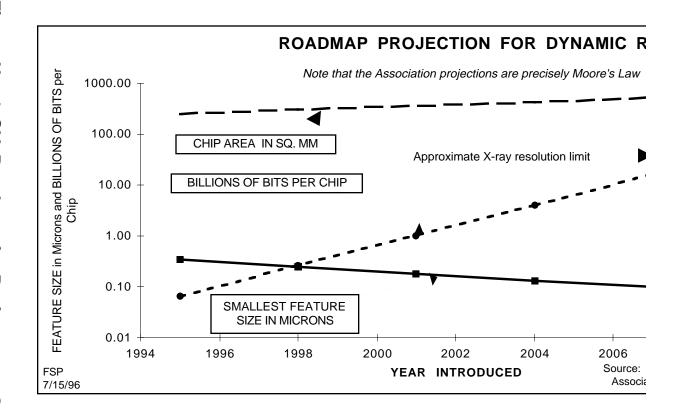


Figure No. 3 Potential Cost for a Petaflops System Today

Figure No. 4 SIA Roadmap for Performance Growth for Dynamic RA



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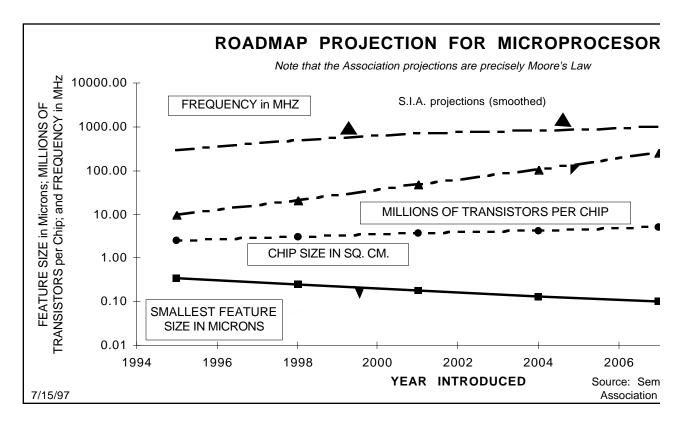
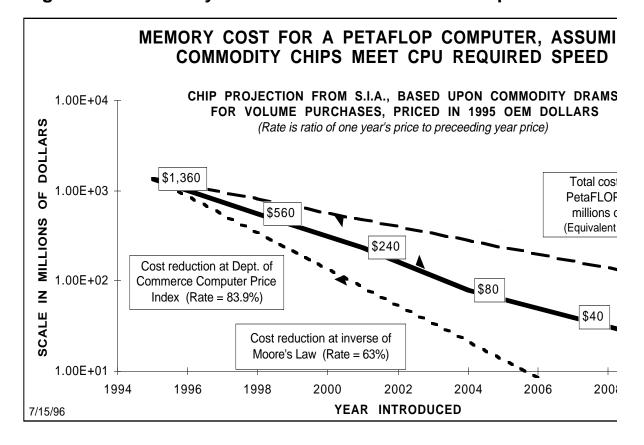


Figure No. 5 SIA Roadmap for Microprocessors

Figure No. 6 Memory Cost Derived from SIA Roadmap for DRAMS



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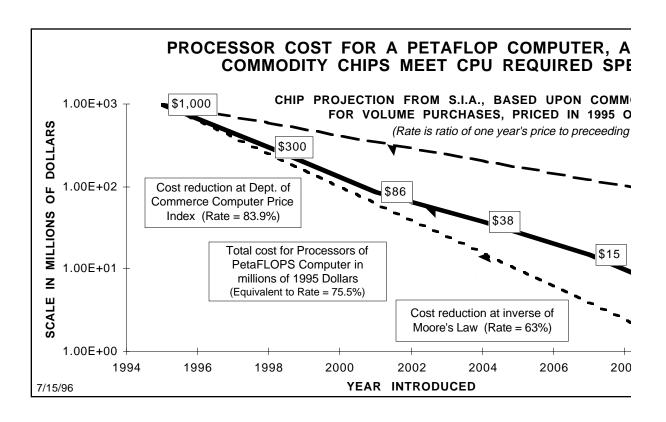
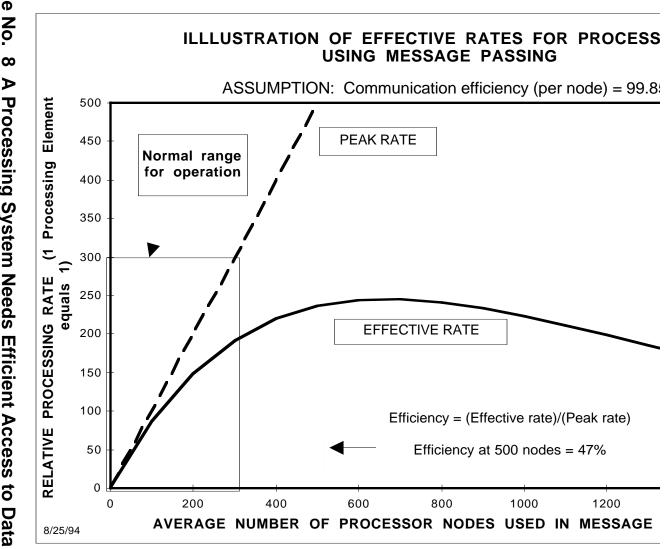


Figure No. 7 Possible Cost for Processors, Assuming Commodity (

Figure No. 8 A Processing System Needs Efficient Access to I



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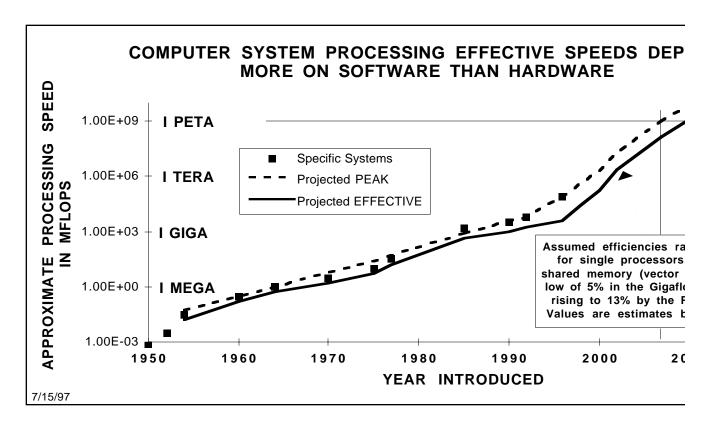
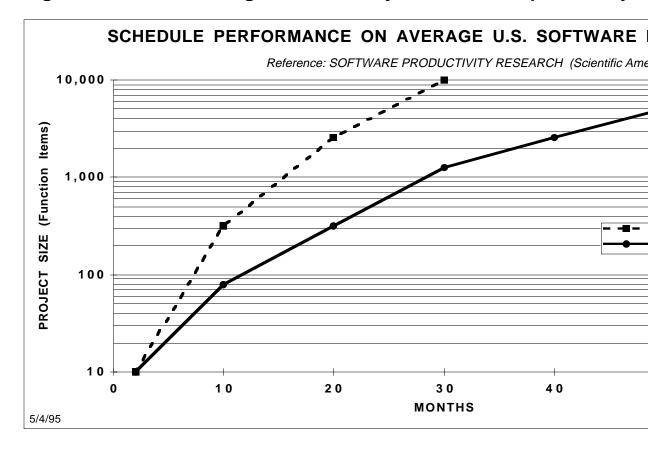


Figure No. 9 Effective Rates Could Be Far Below Peak Rates

Figure No. 10 The Average Software Project Will be Completed Very L



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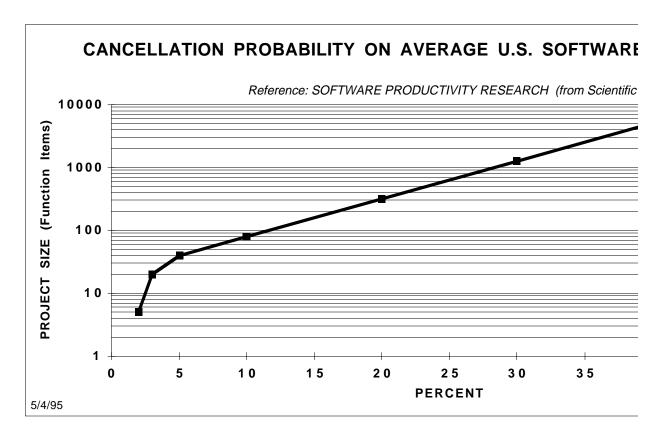


Figure No. 11 The Bigger the Problem, the Less Likely it Will be Fini

## **APPENDINDIX D: FIGURES (Full Size)**